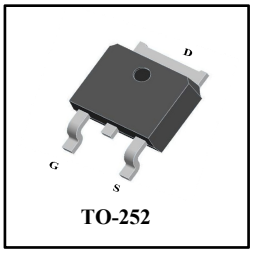
MPVD7N65F
Power MOSFET

FEATURES

- BV_{DSS}: 650V, I_D=7A
- $R_{DS(on)}$: 1.5Ω(Max) @V_{GS}=10V
- Very Low FOM ($R_{DS(on)}$ *Q_g)
- Excellent stability and uniformity

APPLICATIONS

- Switch Mode Power Supply (SMPS)
- Uninterruptible Power Supply (UPS)
- Power Factor Correction (PFC)
- AC to DC Converters



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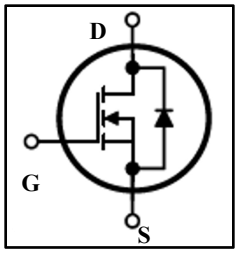


Ordering Information

Type NO.	Marking	Package Code
MPVD7N65F	MPVD7N65F	TO-252

Absolute Maximum Ratings T_C = 25ºC, unless otherwise noted

Parameter		Symbol	Value	Unit
Drain-Source Voltage (V_{GS} = 0V)		V_{DSS}	650	V
Continuous Drain Current		I_D	7	A
Pulsed Drain Current	(note1)	I_{DM}	28	A
Gate-Source Voltage		V_{GSS}	±30	V
Single Pulse Avalanche Energy	(note2)	E_{AS}	230	mJ
Avalanche Current	(note1)	I_{AR}	4	A
Repetitive Avalanche Energy	(note1)	E_{AR}	21.7	mJ
Power Dissipation (T_C = 25ºC)		P_D	97	W
Operating Junction and Storage Temperature Range		T_J, T_{stg}	-55~+150	ºC

Thermal Resistance

Parameter	Symbol	Value	Unit
Thermal Resistance, Junction-to-Case	R_{thJC}	1.29	ºC/W
Thermal Resistance, Junction-to-Ambient	R_{thJA}	60.0	

Specifications T_J = 25ºC, unless otherwise noted

Parameter	Symbol	Test Conditions	Value			Unit
			Min.	Typ.	Max.	
Static						
Drain-Source Breakdown Voltage	$V_{(BR)DSS}$	V_{GS} = 0V, I_D = 250μA	650	--	--	V
Zero Gate Voltage Drain Current	I_{DSS}	V_{DS} = 650V, V_{GS} = 0V, T_J = 25ºC	--	--	1	μA
Gate-Source Leakage	I_{GSS}	V_{GS} = ±30V	--	--	±100	nA
Gate-Source Threshold Voltage	$V_{GS(th)}$	V_{DS} = V_{GS}, I_D = 250μA	2.0	--	4.0	V
Drain-Source On-Resistance (Note4)	$R_{DS(on)}$	V_{GS} = 10V, I_D = 3.5A	--	1.2	1.5	Ω
Dynamic						
Input Capacitance	C_{iss}	V_{GS} = 0V, V_{DS} = 25V, f = 1.0MHz	--	1178	--	pF
Output Capacitance	C_{oss}		--	89	--	
Reverse Transfer Capacitance	C_{rss}		--	10	--	
Total Gate Charge	Q_g	V_{DD} = 400V, I_D = 7.0A, V_{GS} = 10V	--	19	--	nC
Gate-Source Charge	Q_{gs}		--	5	--	
Gate-Drain Charge	Q_{gd}		--	5	--	
Turn-on Delay Time	$t_{d(on)}$	V_{DD} = 325V, I_D = 7.0A, R_G = 25Ω	--	33	--	ns
Turn-on Rise Time	t_r		--	50	--	
Turn-off Delay Time	$t_{d(off)}$		--	42	--	
Turn-off Fall Time	t_f		--	35	--	
Drain-Source Body Diode Characteristics						
Continuous Body Diode Current	I_S	T_C = 25ºC	--	--	7	A
Pulsed Diode Forward Current	I_{SM}		--	--	28	
Body Diode Voltage	V_{SD}	T_J = 25ºC, I_{SD} = 7.0A, V_{GS} = 0V	--	--	1.4	V
Reverse Recovery Time	t_{rr}	V_R = 400V, I_F = 7.0A, di_F/dt = 100A/μs	--	365	--	ns
Reverse Recovery Charge	Q_{rr}		--	3.4	--	μC

Notes

1. Repetitive Rating: Pulse width limited by maximum junction temperature
2. L = 10mH, V_{DD} = 50V, R_G = 25Ω, Starting T_J = 25°C
3. Pulse Test: Pulse width ≤ 300μs, Duty Cycle ≤ 1%
4. Essentially independent of operating temperature

Typical Characteristics T_J = 25ºC, unless otherwise noted

Figure 1. Output Characteristics

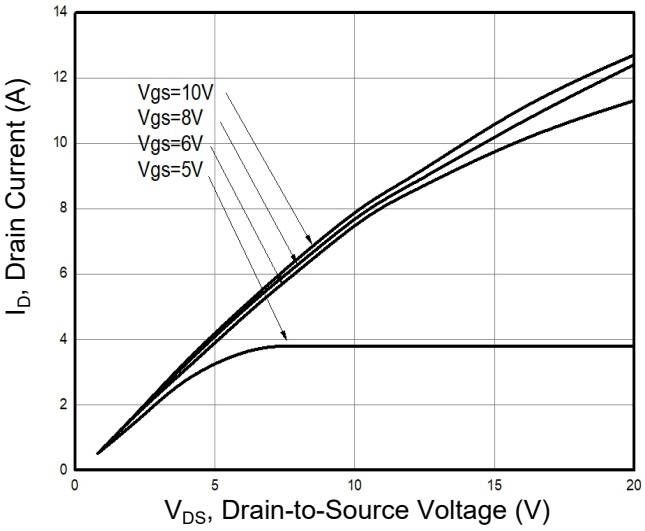


Figure 2. Transfer Characteristics

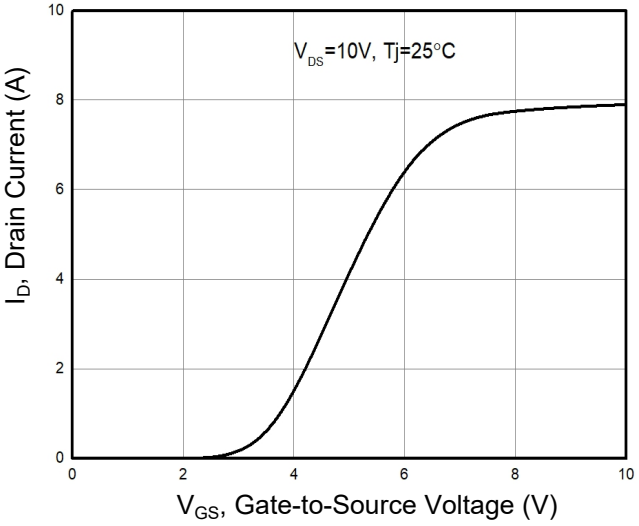


Figure 3. Drain Current vs. Temperature

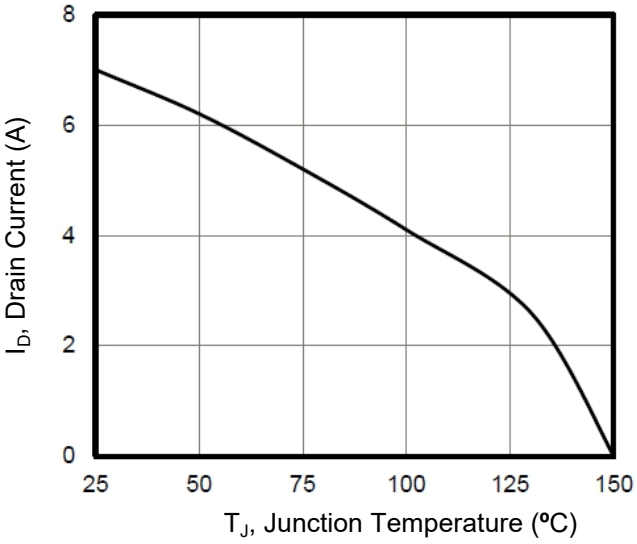


Figure 4. Capacitance

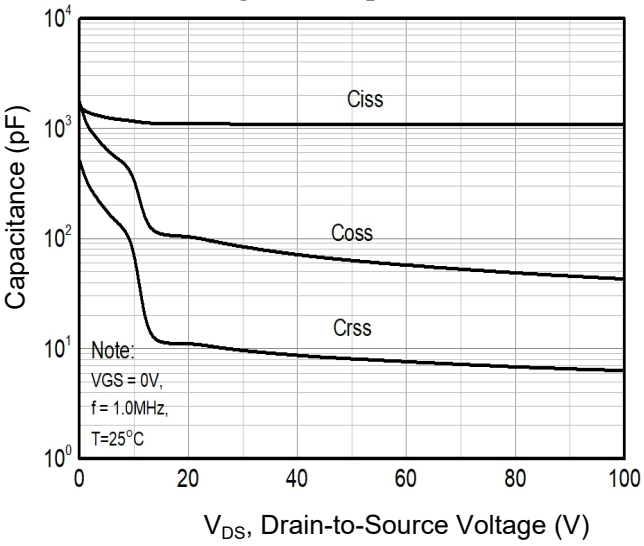


Figure 5. Gate Charge

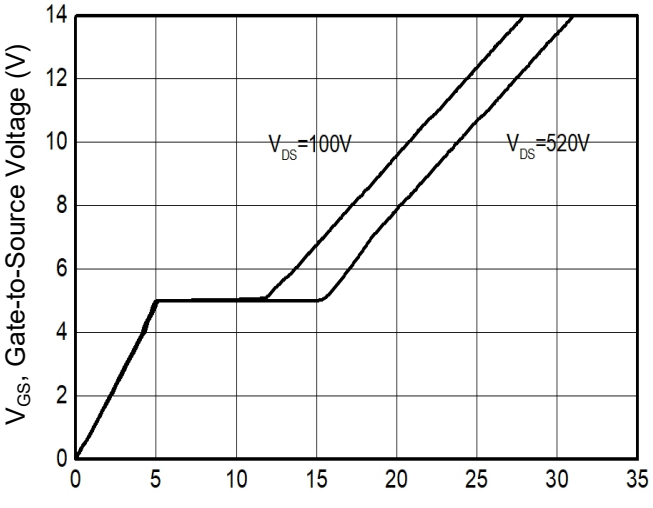
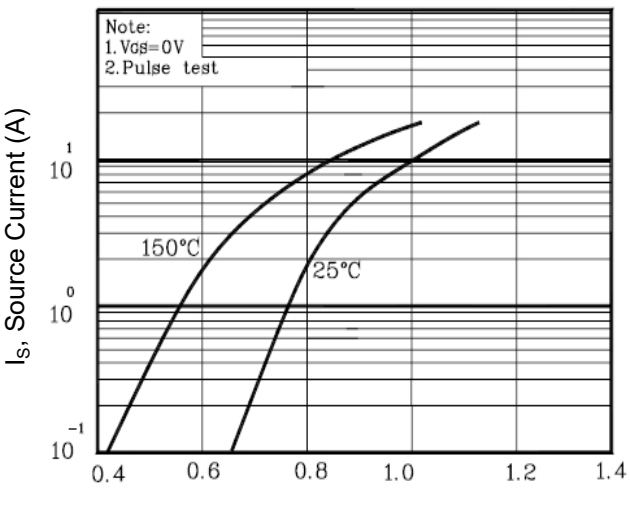


Figure 6. Body Diode Forward Voltage



Typical Characteristics T_J = 25ºC, unless otherwise noted

Figure 7. On-Resistance vs. Temperature

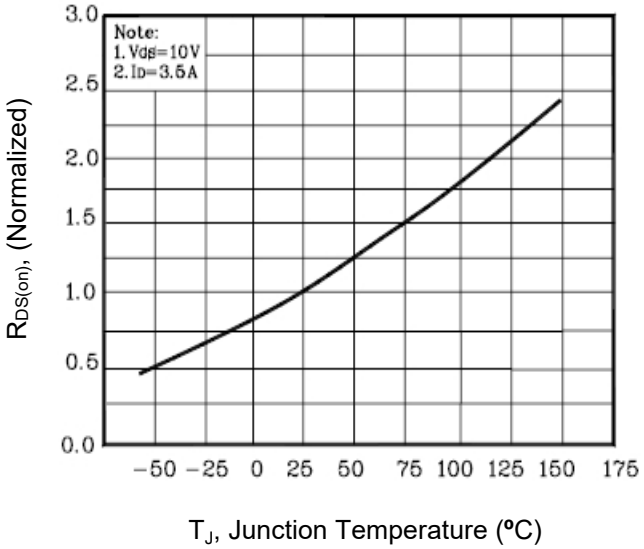


Figure 8. BV_{DSS} vs. Temperature

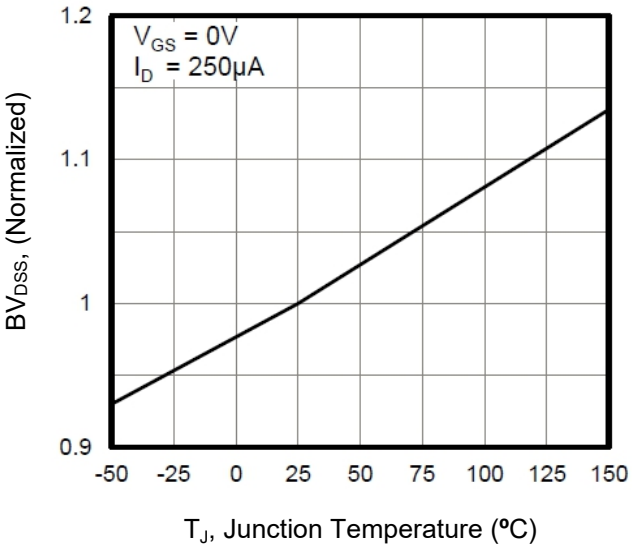


Figure 9. Transient Thermal Impedance

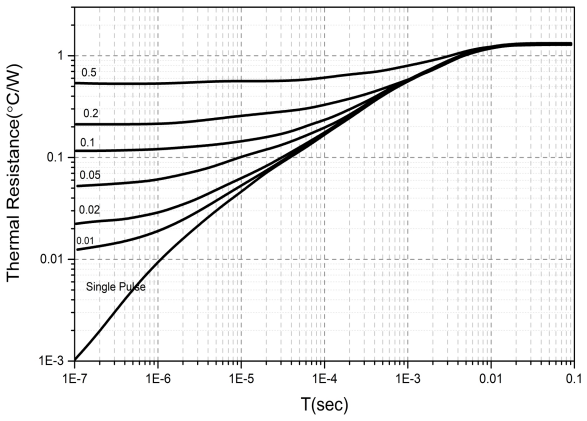


Figure A：Gate Charge Test Circuit and Waveform

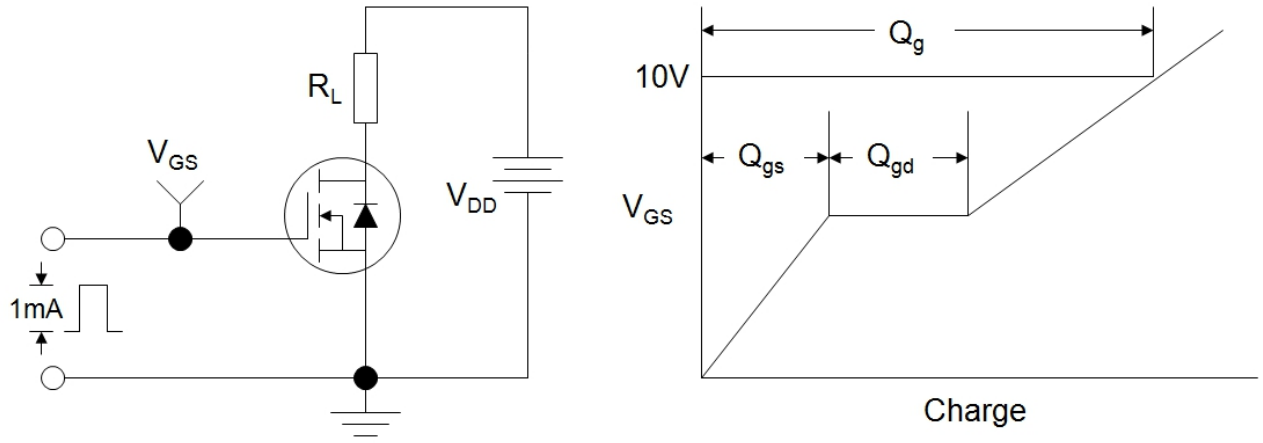


Figure B：Resistive Switching Test Circuit and Waveform

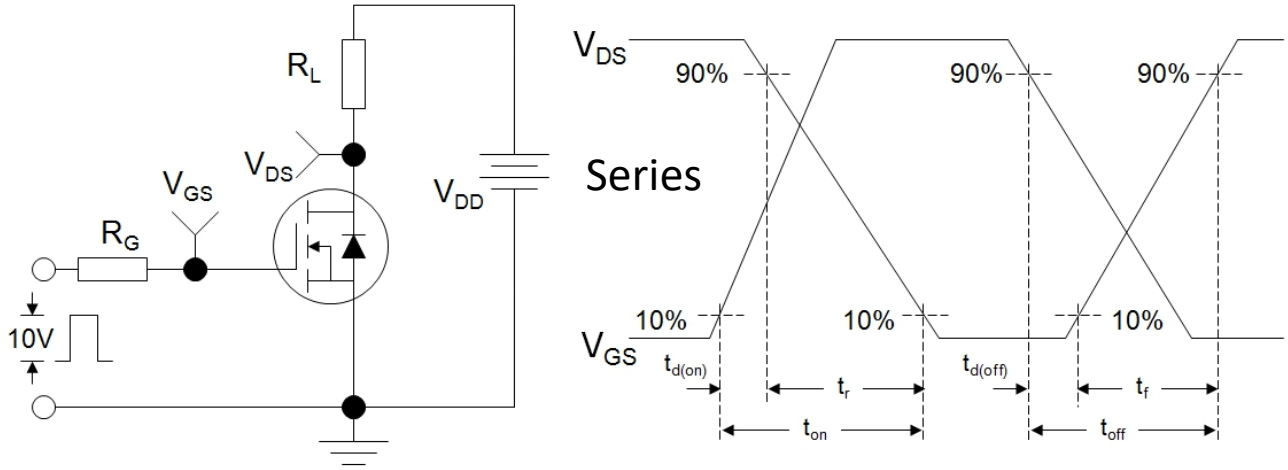
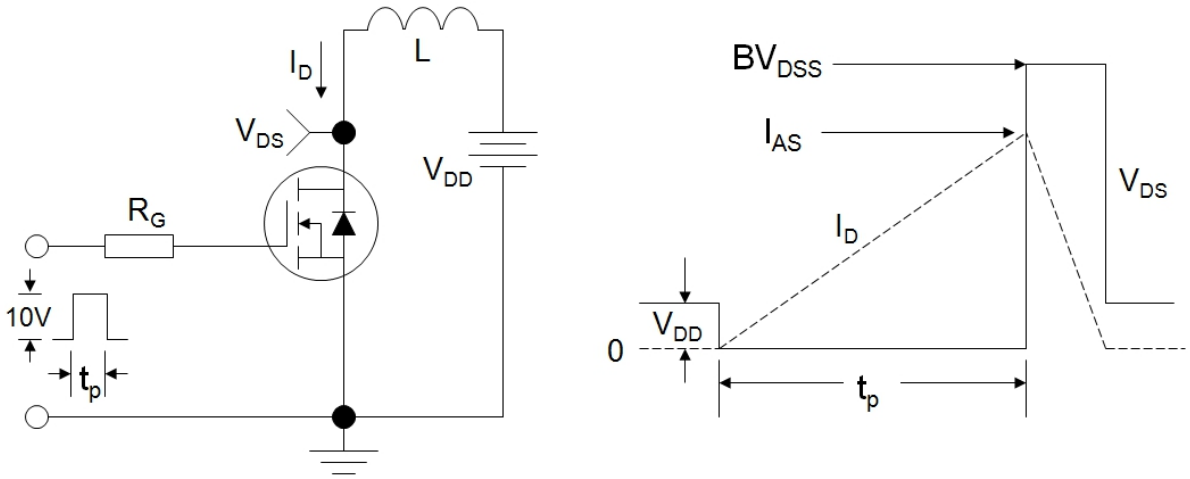


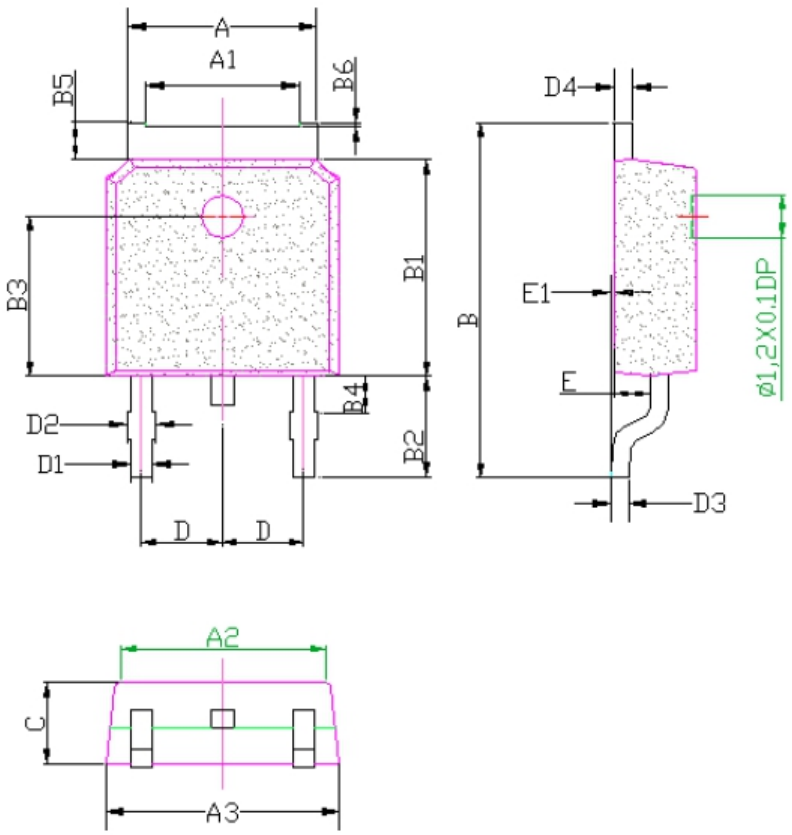
Figure C：Unclamped Inductive Switching Test Circuit and Waveform



Outline Dimension

Unit: mm

TO-252



DIM	MILLIMETERS
A	5.33±0.2
A1	4.33±0.2
A2	5.80±0.1
A3	6.6±0.2
B	10±0.5
B1	6.1±0.3
B2	2.85±0.5
B3	4.5±0.15
B4	1.0±0.1
B5	1.05±0.1
B6	0.1±0.05
C	2.3±0.15
D	2.286±0.05
D1	0.60±0.1
D2	0.72±0.12
D3	0.5±0.08
D4	0.5±0.08
E	1.01±0.15
E1	0.1±0.05
DIA	⊙1.2(deep 0.1)

Unit :mm