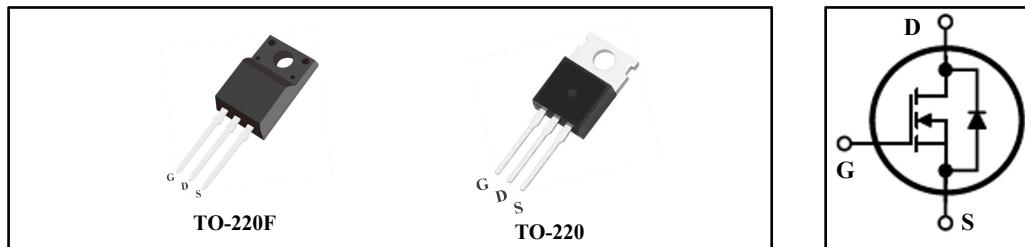


FEATURES

- BV_{DSS} : 650V, $I_D=20A$
- $R_{DS(on)}$: 0.48Ω(Max) @ $V_{GS}=10V$
- Very Low FOM ($R_{DS(on)} * Q_g$)
- Excellent stability and uniformity

APPLICATIONS

- Switch Mode Power Supply (SMPS)
- Uninterruptible Power Supply (UPS)
- Power Factor Correction (PFC)
- AC to DC Converters


Ordering Information

Type NO.	Marking	Package Code	
MPVA20N65F	MPVA20N65F	TO-220F	
MPVP20N65F	MPVP20N65F	TO-220	

Absolute Maximum Ratings $T_C = 25^{\circ}\text{C}$, unless otherwise noted

Parameter	Symbol	Value		Unit
		220F	220	
Drain-Source Voltage ($V_{GS} = 0V$)	V_{DSS}	650		V
Continuous Drain Current	I_D	20		A
Pulsed Drain Current (note1)	I_{DM}	80		A
Gate-Source Voltage	V_{GSS}	± 30		V
Single Pulse Avalanche Energy (note2)	E_{AS}	1450		mJ
Avalanche Current (note1)	I_{AR}	17		A
Repetitive Avalanche Energy (note1)	E_{AR}	90		mJ
Power Dissipation ($T_C = 25^{\circ}\text{C}$)	P_D	120	416	W
Operating Junction and Storage Temperature Range	T_J, T_{stg}	$-55\sim+150$		°C

Thermal Resistance

Parameter	Symbol	Value		Unit
		220F	220	
Thermal Resistance, Junction-to-Case	R_{thJC}	1.04	0.3	°C/W
Thermal Resistance, Junction-to-Ambient	R_{thJA}	62.5	60.0	



懋聲微科技

MPVX20N65F Series

Power MOSFET

Specifications $T_J = 25^\circ\text{C}$, unless otherwise noted

Parameter	Symbol	Test Conditions	Value			Unit
			Min.	Typ.	Max.	
Static						
Drain-Source Breakdown Voltage	$V_{(\text{BR})\text{DSS}}$	$V_{\text{GS}} = 0\text{V}, I_D = 250\mu\text{A}$	650	--	--	V
Zero Gate Voltage Drain Current	I_{DSS}	$V_{\text{DS}} = 650\text{V}, V_{\text{GS}} = 0\text{V}, T_J = 25^\circ\text{C}$	--	--	1	μA
Gate-Source Leakage	I_{GSS}	$V_{\text{GS}} = \pm 30\text{V}$	--	--	± 100	nA
Gate-Source Threshold Voltage	$V_{\text{GS}(\text{th})}$	$V_{\text{DS}} = V_{\text{GS}}, I_D = 250\mu\text{A}$	2.0	--	4.0	V
Drain-Source On-Resistance (Note4)	$R_{\text{DS}(\text{on})}$	$V_{\text{GS}} = 10\text{V}, I_D = 10\text{A}$	--	0.38	0.48	Ω
Dynamic						
Input Capacitance	C_{iss}	$V_{\text{GS}} = 0\text{V}, V_{\text{DS}} = 25\text{V}, f = 1.0\text{MHz}$	--	3500	--	pF
Output Capacitance	C_{oss}		--	240	--	
Reverse Transfer Capacitance	C_{rss}		--	23	--	
Total Gate Charge	Q_g	$V_{\text{DD}} = 520\text{V}, I_D = 20\text{A}, V_{\text{GS}} = 10\text{V}$	--	60	--	nC
Gate-Source Charge	Q_{gs}		--	14	--	
Gate-Drain Charge	Q_{gd}		--	23	--	
Turn-on Delay Time	$t_{\text{d}(\text{on})}$	$V_{\text{DD}} = 325\text{V}, I_D = 20\text{A}, R_G = 25\Omega$	--	38	--	ns
Turn-on Rise Time	t_r		--	70	--	
Turn-off Delay Time	$t_{\text{d}(\text{off})}$		--	180	--	
Turn-off Fall Time	t_f		--	85	--	
Drain-Source Body Diode Characteristics						
Continuous Body Diode Current	I_S	$T_C = 25^\circ\text{C}$	--	--	20	A
Pulsed Diode Forward Current	I_{SM}		--	--	80	
Body Diode Voltage	V_{SD}	$T_J = 25^\circ\text{C}, I_{\text{SD}} = 10.0\text{A}, V_{\text{GS}} = 0\text{V}$	--	--	1.4	V
Reverse Recovery Time	t_{rr}	$V_R = 400\text{V}, I_F = 20.0\text{A}, dI_F/dt = 100\text{A}/\mu\text{s}$	--	450	--	ns
Reverse Recovery Charge	Q_{rr}		--	7.1	--	μC

Notes

1. Repetitive Rating: Pulse width limited by maximum junction temperature
2. $I_{\text{AS}} = 17\text{A}, V_{\text{DD}} = 50\text{V}, R_G = 25\Omega$, Starting $T_J = 25^\circ\text{C}$
3. Pulse Test: Pulse width $\leq 300\mu\text{s}$, Duty Cycle $\leq 1\%$
4. Essentially independent of operating temperature

Typical Characteristics $T_J = 25^\circ\text{C}$, unless otherwise noted

Figure 1. Output Characteristics

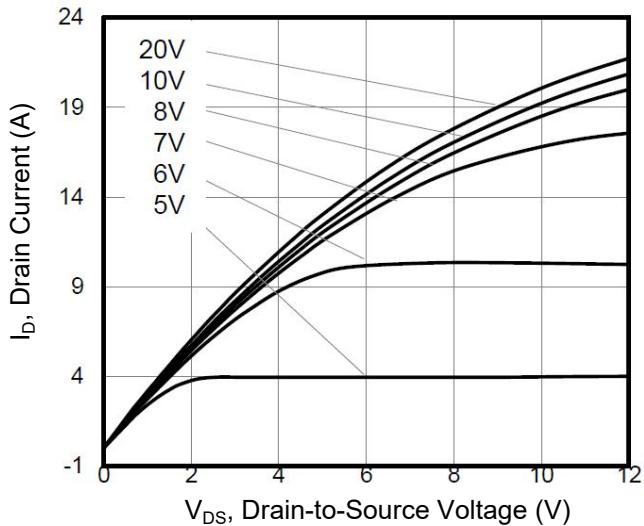


Figure 2. Transfer Characteristics

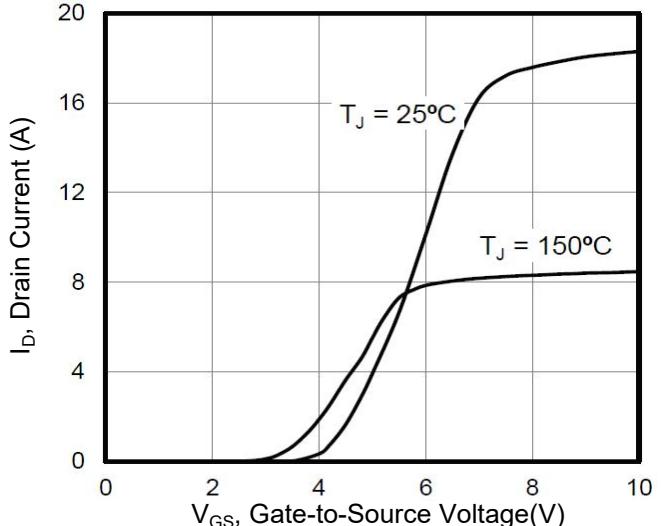


Figure 3. BV_{DSS} vs. Temperature

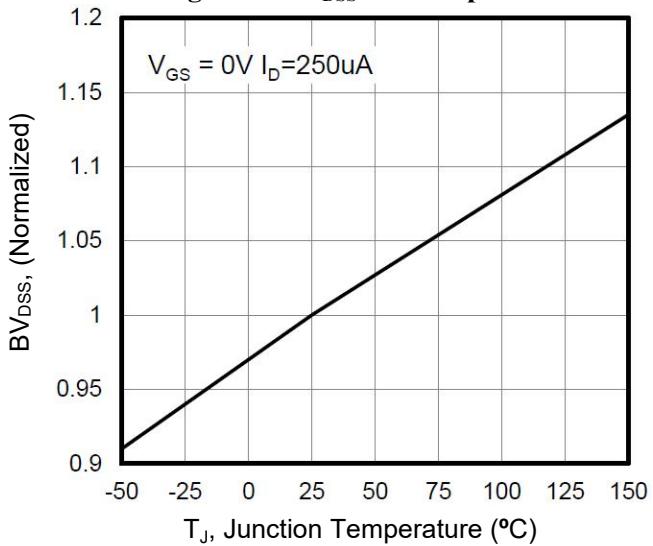


Figure 4. On-Resistance vs. Temperature

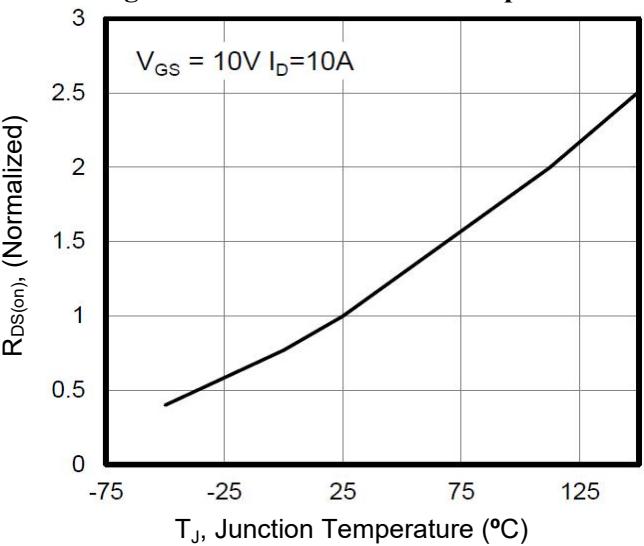


Figure 5. Gate Charge

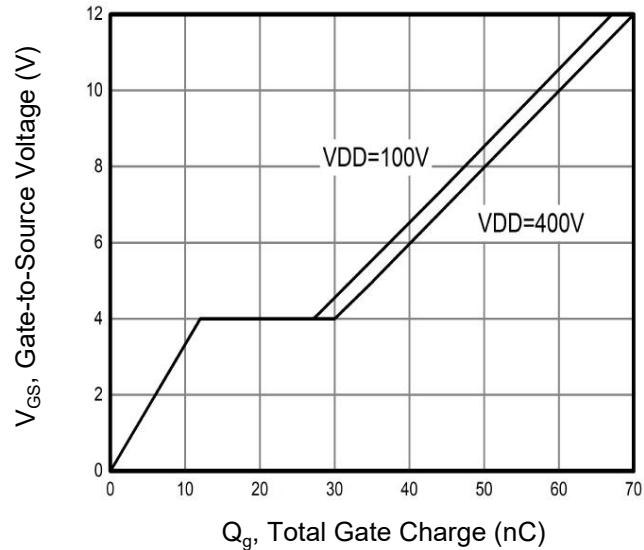
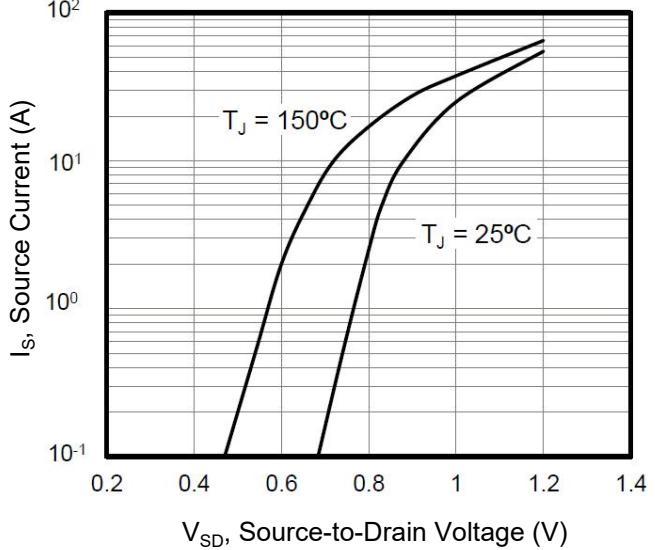


Figure 6. Body Diode Forward Voltage



Typical Characteristics $T_J = 25^\circ\text{C}$, unless otherwise noted

Figure 7. Drain Current vs. Temperature

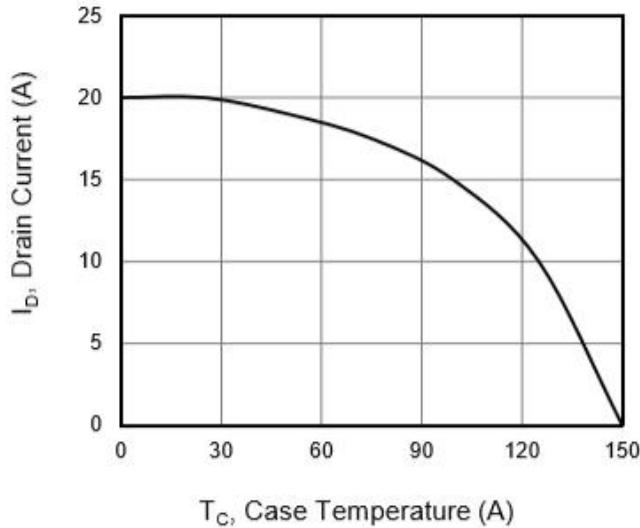
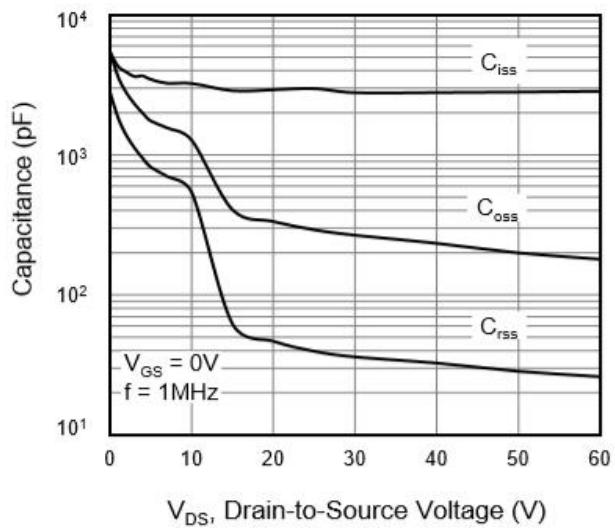
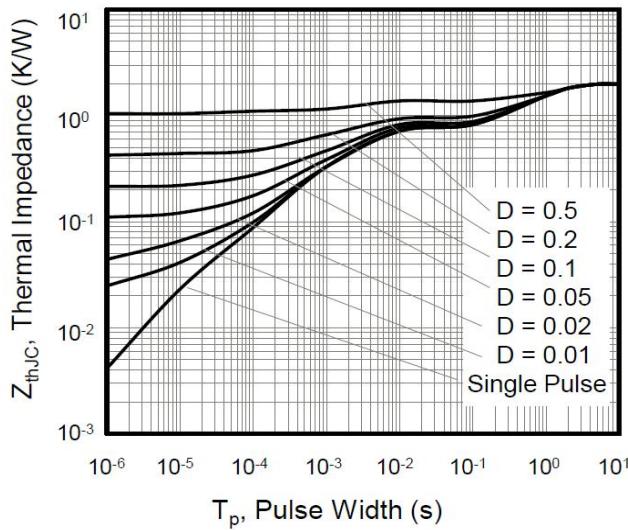


Figure 8. Capacitance



**Figure 9. Transient Thermal Impedance
(TO-220F)**



**Figure 10. Transient Thermal Impedance
(TO-220)**

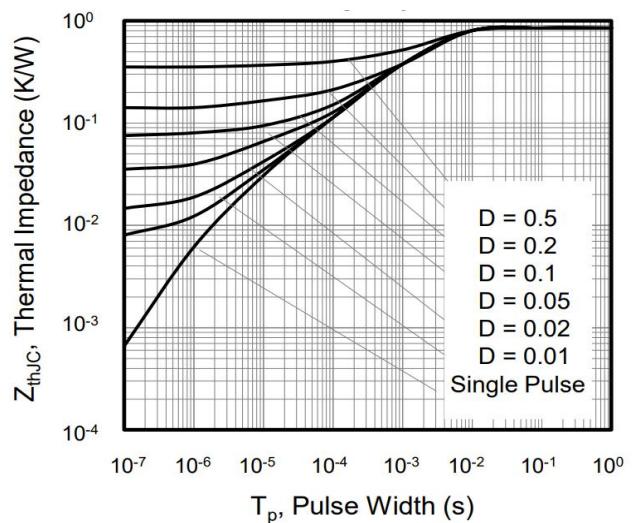
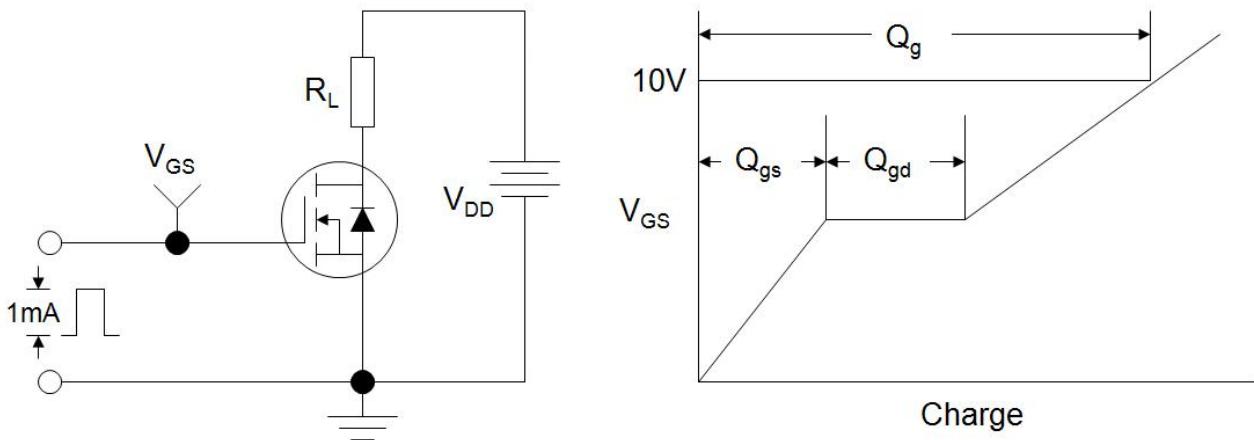
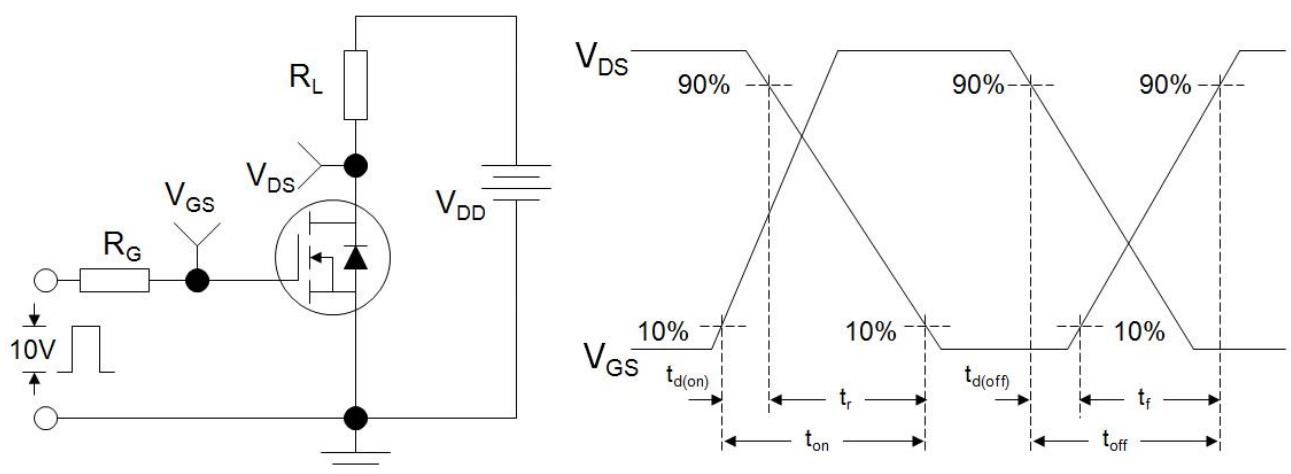
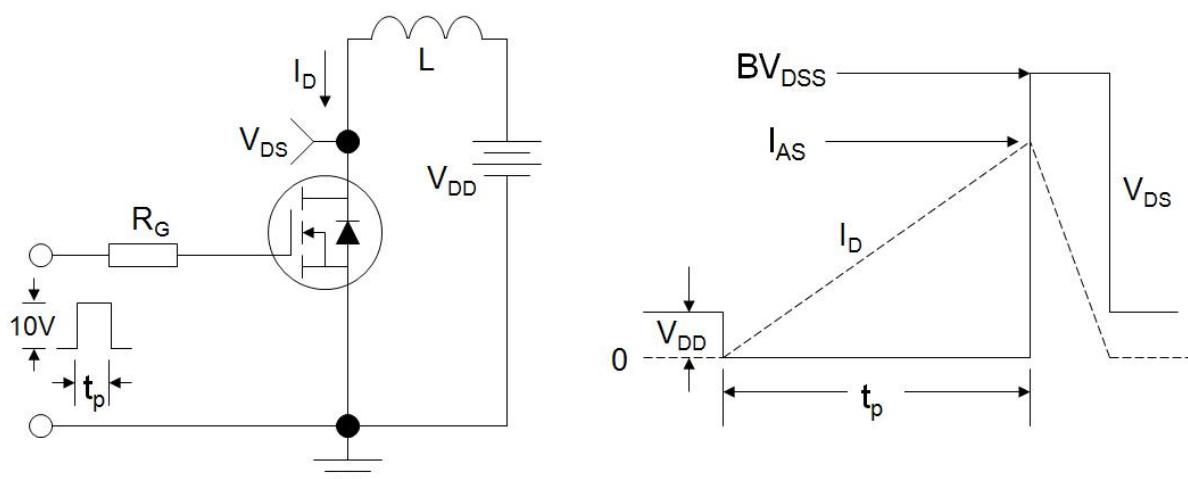
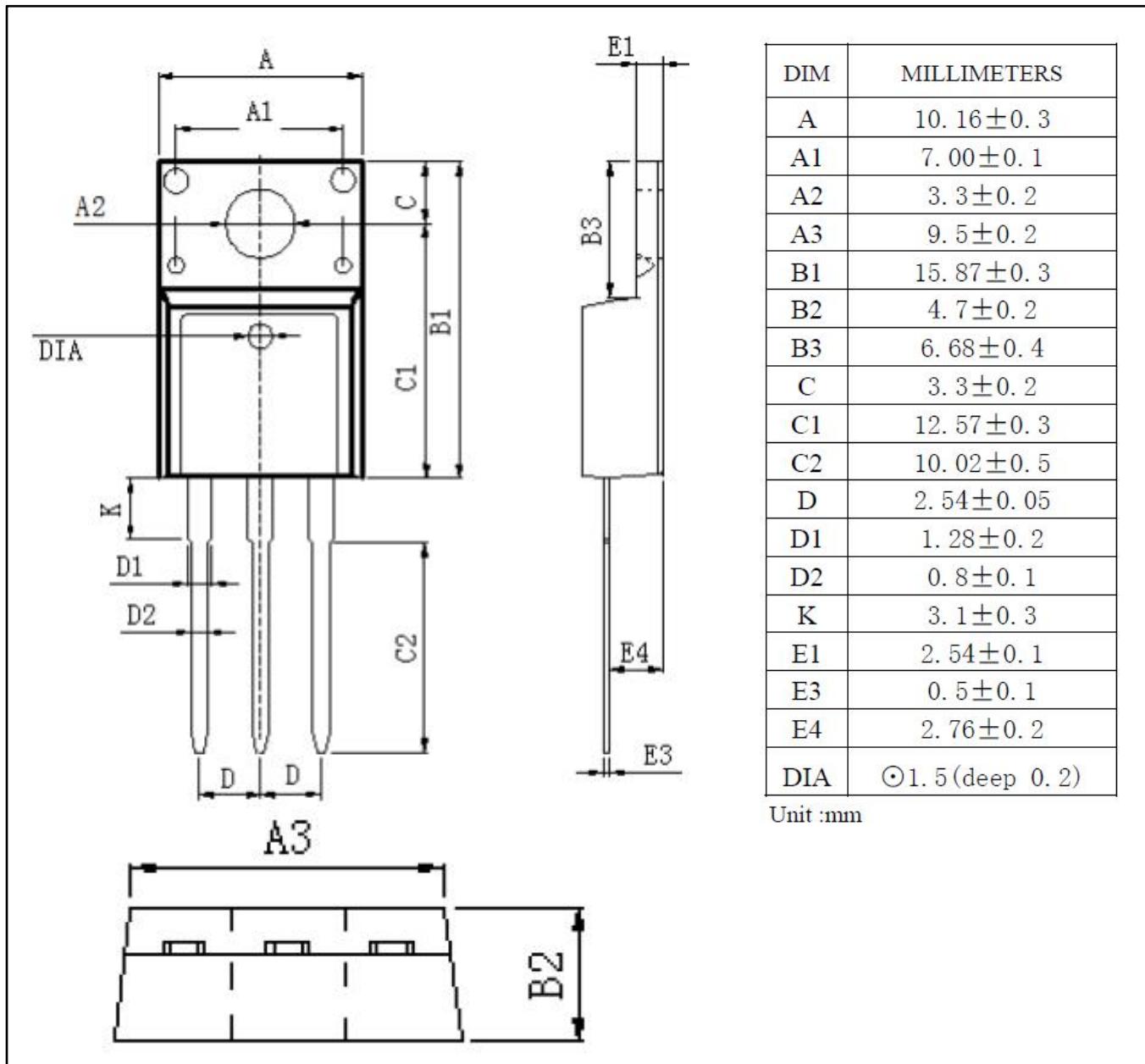


Figure A: Gate Charge Test Circuit and Waveform

Figure B: Resistive Switching Test Circuit and Waveform

Figure C: Unclamped Inductive Switching Test Circuit and Waveform


Outline Dimension

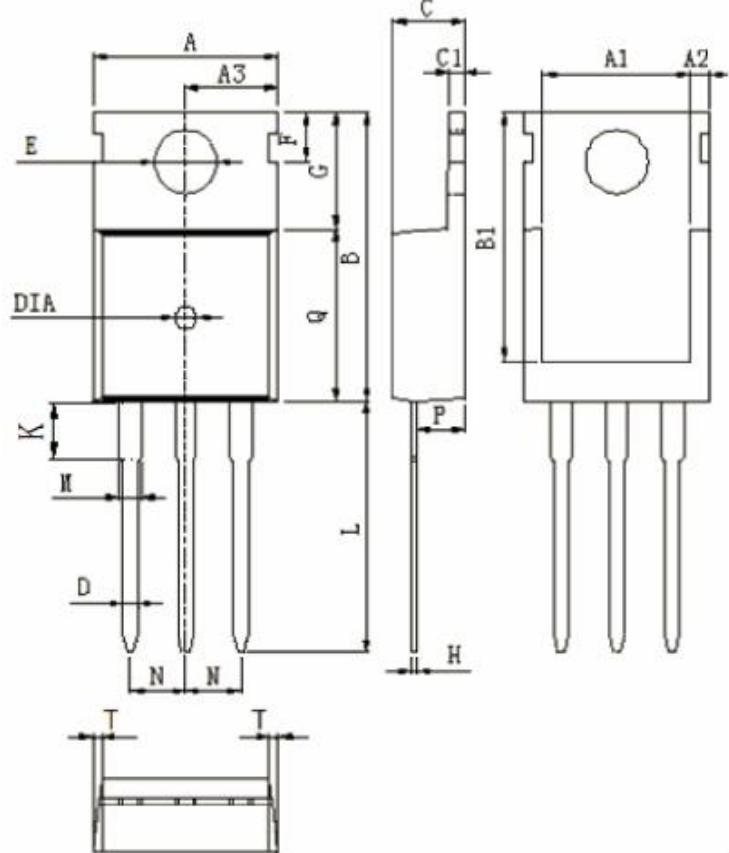
Unit: mm

TO-220F



Outline Dimension

Unit: mm

TO-220


DIM	MILLIMETERS
A	10.0±0.3
A1	8.64±0.2
A2	1.15±0.1
A3	5.0±0.2
B	15.8±0.4
B1	13.2±0.3
C	4.56±0.1
C1	1.3±0.2
D	0.8±0.2
E	3.6±0.2
F	2.95±0.3
G	6.5±0.3
H	0.5±0.1
K	3.1±0.2
L	13.2±0.4
M	1.25±0.1
N	2.54±0.1
P	2.4±0.3
Q	9.0±0.3
T	W:0.35
DIA	◎1.5(deep 0.2)

Unit :mm